



☐ Tentative Specification
☒ Preliminary Specification
☐ Specification Approval

Specification For 2.13" EPD

Model Name: RSE0213N01

Version: V0

	PREPARED BY	CHECKED BY	APPROVED BY
SIGNATURE	JY Guo		
DATE			

CUSTOMER APPROVAL	SIGNATURE	DATE
	Notes:	

Notes :

- 1、Please contact before assigning your product based on this module specification.
- 2、To improve the quality of product, and this product specification is subject to change without any notice.

REVISION RECORD

[illegible]

CONTENTS

1. General Description	4
2. Features	4
3. Application.....	4
4. Mechanical Specification.....	4
4.1 Dimension	4
4.2 Mechanical Drawing of EPD Module.....	5
6. Electrical Characteristics.....	7
6.1 Absolute Maximum Rating	7
6.2 Panel DC Characteristics	7
6.3 Panel DC Characteristics (Driver IC Internal Regulators).....	8
6.4 MCU Interface	错误!未定义书签。
7. Optical Specification	11
8. Handling, Safety, and Environment Requirements	11
9. Reliability Test.....	12
10. Block Diagram	13
11. Typical Application Circuit with SPI Interface	14
12. Packaging.....	14
13. Mark and Bar Code Definition	14

1. General Description

SE0213N01 is an Active Matrix Electrophoretic Display(AM EPD), with interface and a reference system design. The display is capable to display images at 1-bit white and black full display capabilities. The 2.13” active area contains 122x250 pixels. The module is a TFT-array driving electrophoretic display, with integrated circuits including gate buffer, source buffer, MCU interface, timing control logic, oscillator, DC-DC, SRAM, LUT, VCOM. Module can be used in portable electronic devices, such as Electronic Shelf Label (ESL) System.

2. Features

- ◆ 122×250 pixels display
- ◆ White reflectance above 30%
- ◆ Contrast ratio above 8:1
- ◆ Ultra wide viewing angle
- ◆ Ultra low power consumption
- ◆ Pure reflective mode
- ◆ Bi-stable display
- ◆ Commercial temperature range
- ◆ Landscape, portrait modes
- ◆ Hard-coat antiglare display surface
- ◆ Ultra Low current deep sleep mode
- ◆ On chip display RAM
- ◆ Waveform can stored in On-chip OTP or written by MCU
- ◆ Serial peripheral interface available
- ◆ On-chip oscillator
- ◆ On-chip booster and regulator control for generating VCOM, Gate and Source driving voltage
- ◆ Built-in temperature sensor

3. Application

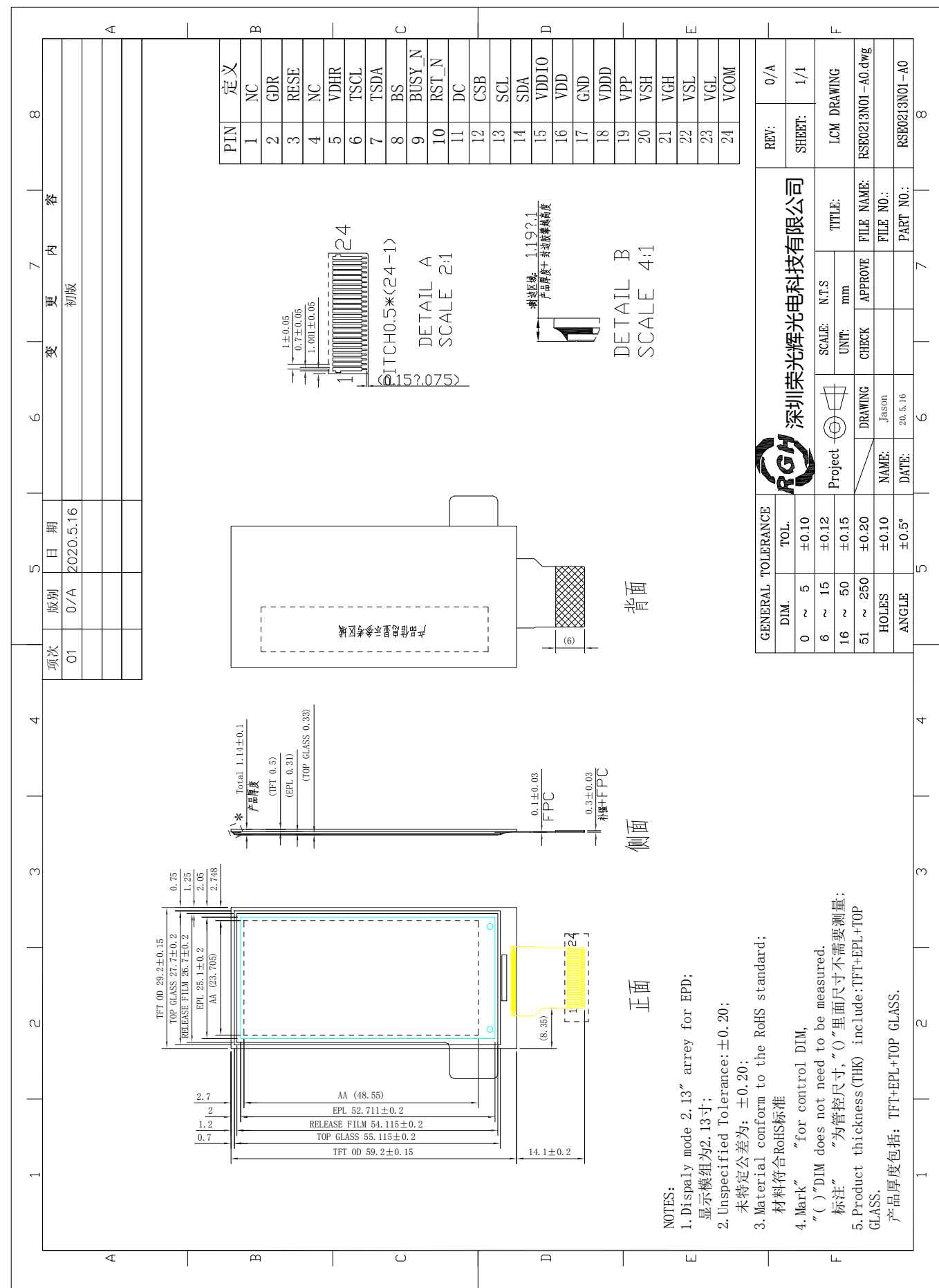
Electronic Shelf Label System

4. Mechanical Specification

4.1 Dimension

No.	Item	Contents	Unit
1	Screen Size	2.13 (Diagonal)	inch
2	Resolution(H*V)	122*250	Pixels
3	Pixel pitch (L*W)	TBD	mm
4	Interface type	SPI interface	/
5	Active area (L*W)	23.70*48.55	mm
6	Outline Dimension (L*W*H)	29.2*59.2*1.14	mm
7	Weight	4.1	Tg

4.2 Mechanical Drawing of EPD Module



5. Input/output Pin Assignment

No.	Name	I/O	Description	Remark
1	NC		Do not connect with other NC pins	
2	GDR	O	N-Channel MOSFET Gate Drive Control	
3	RESE	I	Current Sense Input for the Control Loop	
4	NC		Do not connect with other NC pins	
5	VDHR	C	Positive source driver voltage for Source	
6	TSCL	O	I ² C Interface to digital temperature sensor Clock pin	
7	TSDA	I/O	I ² C Interface to digital temperature sensor Data pin	
8	BS	I	Bus Interface selection pin	Note 5-4
9	BUSY_N	O	Busy state output pin	Note 5-3
10	RST_N	I	Reset signal input. Active Low.	
11	DC	I	Data /Command control pin	Note 5-2
12	CSB	I	Chip select input pin	Note 5-1
13	SCL	I	Serial Clock pin (SPI)	
14	SDA	I	Serial Data pin (SPI)	
15	VDDIO	P	Power Supply for interface logic pins. It should be connected with VDD	
16	VDD	P	Power Supply for the chip	
17	GND	P	Ground	
18	VDDD	C	Core logic power pin VDD can be regulated internally from VCI. A capacitor should be connected between VDD and VSS under all circumstances	
19	VPP	P	Power Supply for OTP Programming	
20	VSH	C	Positive Source driving voltage	
21	VGH	C	Positive Gate driving voltage	
22	VSL	C	Negative Source driving voltage	
23	VGL	C	Negative Gate driving voltage	
24	VCOM	C	VCOM driving voltage	

I = Input Pin, O =Output Pin, I/O = Bi-directional Pin (Input/Output), P = Power Pin, C = Capacitor Pin

Note 5-1: This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CSB is pulled LOW.

Note 5-2: This pin is Data/Command control pin connecting to the MCU in 4-wire SPI mode. When the pin is pulled HIGH, the data at SDA will be interpreted as data. When the pin is pulled LOW, the data at SDA will be interpreted as command.

Note 5-3: This pin is Busy state output pin. When Busy is High, the operation of chip should not be interrupted, command should not be sent, e.g., The chip would put Busy pin High when

- Outputting display waveform
- Programming with OTP
- Communicating with digital temperature

sensor Note 5-4: Bus interface selection pin

BS State	MCU Interface
L	4-lines serial peripheral interface(SPI)
H	3- lines serial peripheral interface(SPI) - 9 bits SPI

6. Electrical Characteristics

6.1 Absolute Maximum Rating

Parameter	Symbol	Rating	Unit
Logic supply voltage	V _{dd}	-0.5 to +4.0	V
Logic Input voltage	V _{IN}	-0.5 to V _{dd} +0.5	V
Logic Output voltage	V _{OUT}	-0.5 to V _{dd} +0.5	V

Note: Maximum ratings are those values beyond which damages to the device may occur. Functional operation should be restricted to the limits in the Panel DC Characteristics tables.

6.2 Panel DC Characteristics

The following specifications apply for: GND=0V, VDD=3.0V, T_{OPR} =25°C.

Parameter	Symbol	Condition	Applicable pin	Min.	Typ.	Max.	Unit
Logic supply voltage	V _{CI}	-	VDD	2.4	3.0	3.6	V
High level input voltage	V _{IH}	-	-	0.8 V _{dd}	-	-	V
Low level input voltage	V _{IL}	-	-	-	-	0.2 V _{dd}	V
High level output voltage	V _{OH}	IOH = -100uA	-	0.9 V _{dd}	-	-	V
Low level output voltage	V _{OL}	IOL = 100uA	-	-	-	0.1 V _{dd}	V
OTP Program Voltage	V _{PP}	-	VPP	-	8.25	-	V
Typical power panel	P _{TYP}	-	-	-	12	60	mW
Deep sleep mode	P _{STPY}	-	-	-	3	-	mW
Typical operating current	Iopr_VDD	V _{dd} =3.0V	-	-	4.0	20	mA

Parameter	Symbol	Condition	Applicable pin	Min.	Typ.	Max.	Unit
Sleep mode current	Islp_VDD	VDD=3.0V DC/DC OFF No clock No output load Ram data retain	VDD	-	26	-	uA
Deep sleep mode current	Idslp_VDD	VDD=3.0V DC/DC OFF No clock No output load Ram data not retain	VDD	-	1	-	uA
Operation temperature	T _{OPR}	-	-	0	-	3	°C
Operation illuminance intensity	E	Indoor Only	-	-	-	2000	lux
Operating relative humidity	RHop	-	-	-	-	70	%RH
Storage temperature	TSTG	-	-	-25	-	60	°C
Storage relative humidity	RHst	-	-	30	-	60	%RH

Notes: 1. The typical power is measured with following transition from horizontal 2 gray scale pattern to vertical

- 2 gray scale pattern. (Figure 6-1)
- The deep sleep power is the consumed power when the panel controller is in deep sleepmode.
 - The listed electrical/optical characteristics are only guaranteed under the controller & waveform provided by .

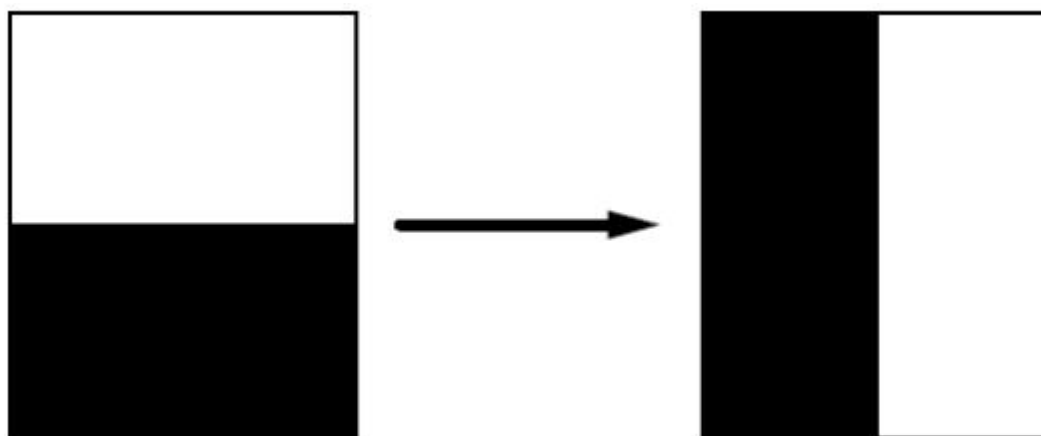


Figure 6-1 The typical power consumption measure pattern

6.3 Panel DC Characteristics (Driver IC Internal Regulators)

The following specifications apply for: VSS=0V, VCI=3.0V, T_{OPR} =25°C.

Parameter	Symbol	Condition	Applicable pin	Min.	Typ.	Max.	Unit
VCOM output voltage	VCOM	-	VCOM		-1.2		V

6.4 Panel AC Characteristics

6.4.1 MCU Interface Selection

MCU interface consist of 2data/command pins and 3 control pins. The pin assignment at different interface mode is summarized in Table 6-4-1. Different MCU mode can be set by hardware selection on BS pins. The display panel only supports 4-wire SPI or 3-wire interface mode.

BS	Interface	CSB	DC	SCL	SDA
High	3-wire SPI	Available	Fix to GND	Available	Available
Low	4-wire SPI	Available	Available	Available	Available

Table 6-4-1: MCU interface assignment under different bus interface mode

6.4.2 MCU Serial Interface (4-wire SPI)

The serial interface consists of serial clock SCLK, serial data SDIN, D/C, CSB. In 4-wire SPI mode, SCL acts as SCLK, SDA acts as SDIN.

Function	CSB	D/C	SCLK
Write command	L	L	↑
Write data	L	H	↑

Note: ↑ stands for rising edge of signal

Table 6-4-2: Control pins of 4-wire Serial interface

SDIN is shifted into an 8-bit shift register on every rising edge of SCLK in the order of D7, D6, ... D0. D/C is sampled on every eighth clock and the data byte in the shift register is written to the Graphic Display Data RAM (RAM) or command register in the same clock.

Under serial mode, only write operations are allowed.

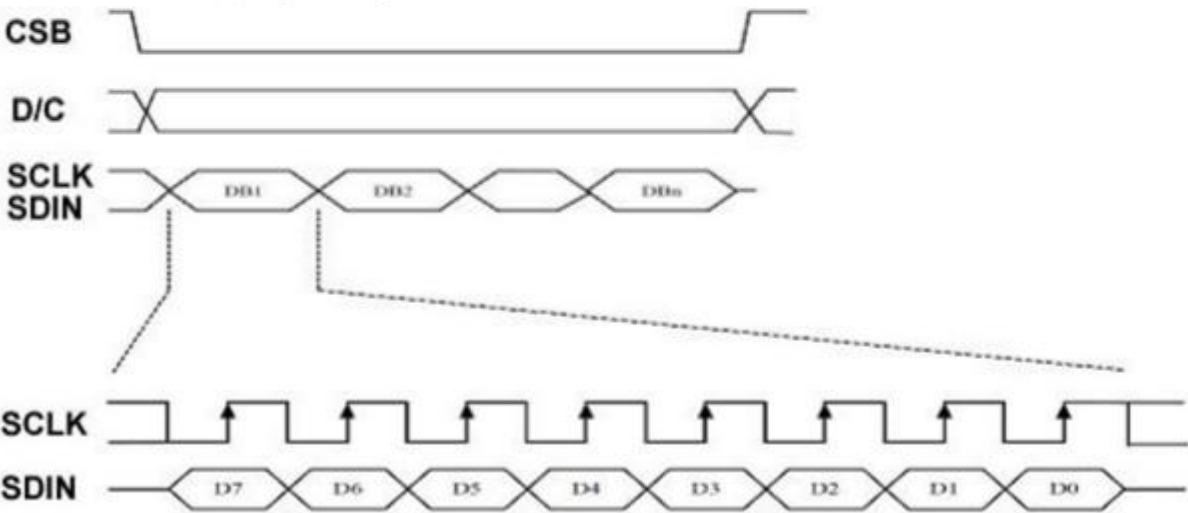


Figure 6-4-2: Write procedure in 4-wire SPI mode

6.4.3 MCU Serial Interface (3-wire SPI)

The 3-wire serial interface consists of serial clock SCLK, serial data SDIN and CSB. In 3-wire SPI mode, SCL acts as SCLK, SDA acts as SDIN.

The operation is similar to 4-wire serial interface while D/C pin is not used. There are altogether 9-bits will be shifted into the shift register on every ninth clock in sequence: D/C bit, D7 to D0 bit. The D/C bit (first bit of the sequential data) will determine the following data byte in the shift register is written to the Display Data RAM (D/C bit = 1) or the command register (D/C bit = 0).

Under serial mode, only write operations are allowed.

Function	CSB	D/C	SCLK
Write command	L	Tie	↑
Write data	L	Tie	↑

Note: ↑ stands for rising edge of signal

Table 6-4-3: Control pins of 3-wire Serial interface

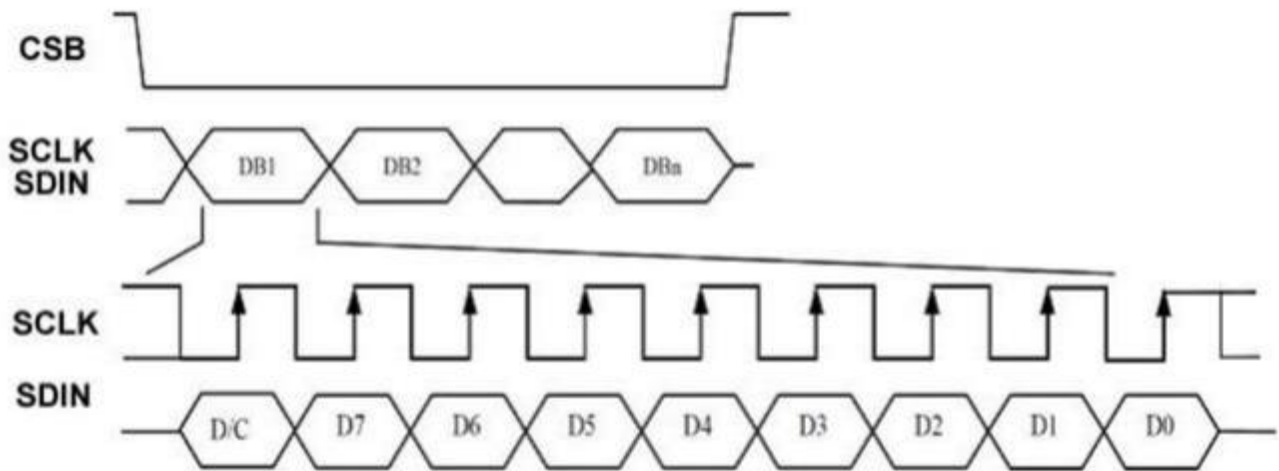


Figure 6-4-3: Write procedure in 3-wire SPI mode

6.4.4 Interface Timing

The following specifications apply for: VSS=0V, VDD =3.0V, TOPR =25°C.

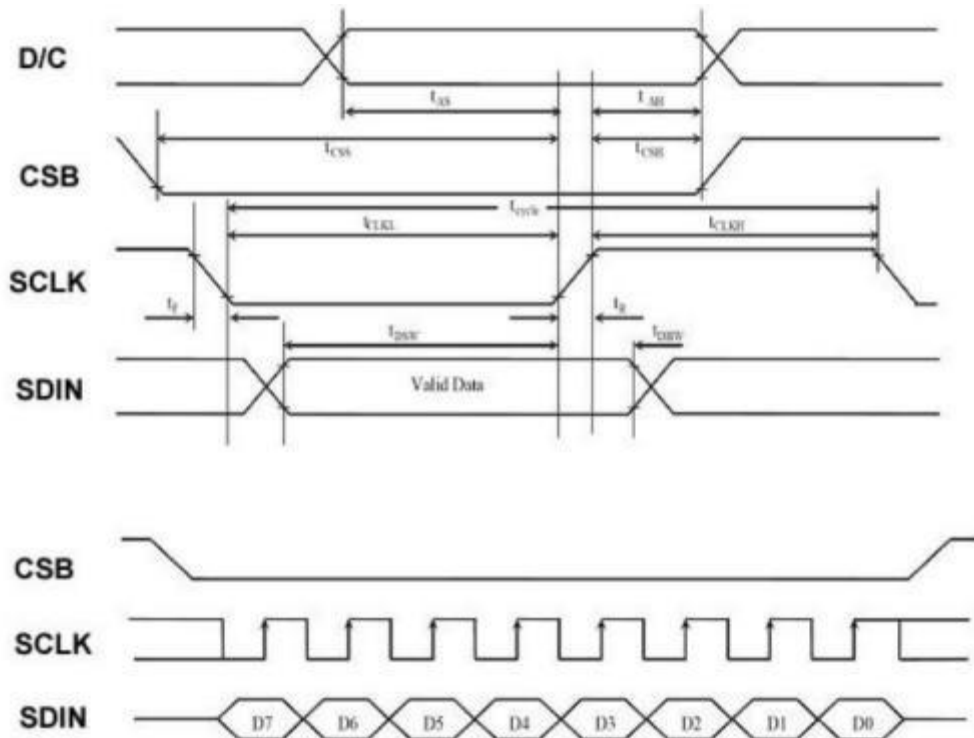


Figure 6-4-4: Serial interface characteristics

(V_{dd} - VSS = 2.4V to 3.3V, TOPR = 25°C, CL=20pF)

Symbol	Parameter	Min.	Typ.	Max.	Unit
t _{cycle}	Clock Cycle Time	250	-	-	ns
t _{AS}	Address Setup Time	150	-	-	ns
t _{AH}	Address Hold Time	150	-	-	ns
t _{CSS}	Chip Select Setup Time	120	-	-	ns
t _{CSH}	Chip Select Hold Time	60	-	-	ns
t _{DSW}	Write Data Setup Time	50	-	-	ns
t _{DHW}	Write Data Hold Time	15	-	-	ns
t _{CLKL}	Clock Low Time	100	-	-	ns
t _{CLKH}	Clock High Time	100	-	-	ns
t _R	Rise Time [20% ~ 80%]	-	-	15	ns
t _F	Fall Time [20% ~80%]	-	-	15	ns

Table 6-4-4: Serial Interface Timing Characteristics

7. Optical Specification

Measurements are made with that the illumination is under an angle of 45 degrees, the detection is perpendicular unless otherwise specified.

Symbol	Parameter	Conditions	Values			Units	Notes
			Min.	Typ.	Max		
R	White Reflectivity	White	30	35	-	%	7-1
CR	Contrast Ratio		8:1	10:1	-	-	7-2
White Δ L 24h	Reduce		-	≤ 4	-	-	-
T _{update}	Image update time	at 25 °C	-	TBD	-	ms	-

Notes: 7-1. Luminance meter: Eye-One Pro Spectrophotometer.

7-2. CR=Surface Reflectance with all white pixel/Surface Reflectance with all black pixels.

8. Handling, Safety, and Environment Requirements

Warning

The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the electrophoretic material. In case of contact with electrophoretic material, wash with water and soap.

Caution

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronic components. Disassembling the display module.

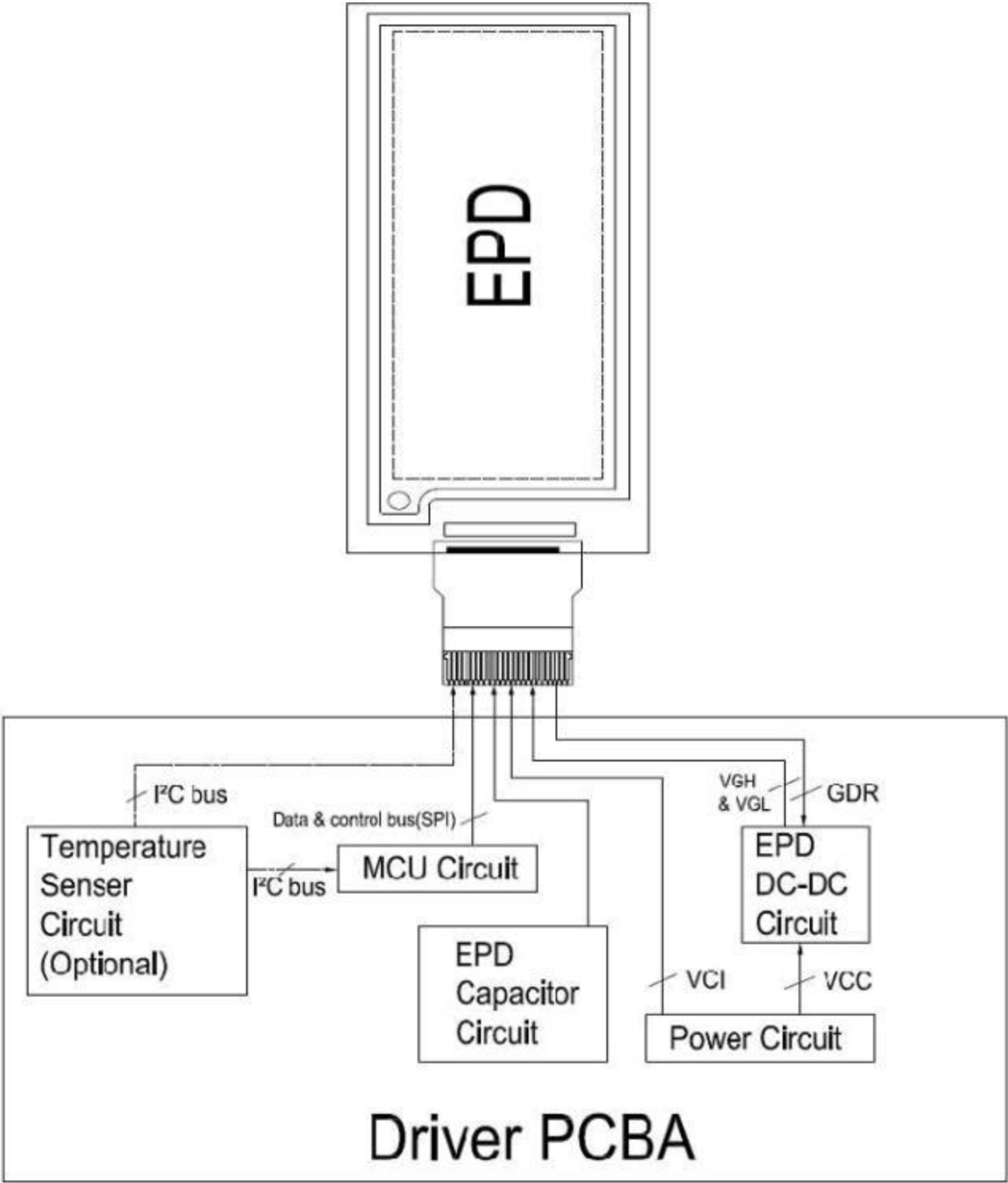
Disassembling the display module can cause permanent damage and invalidates the warranty agreements.

Observe general precautions that are common to handling delicate electronic components. The glass can break and front surfaces can easily be damaged. Moreover the display is sensitive to static electricity and other rough environmental conditions.

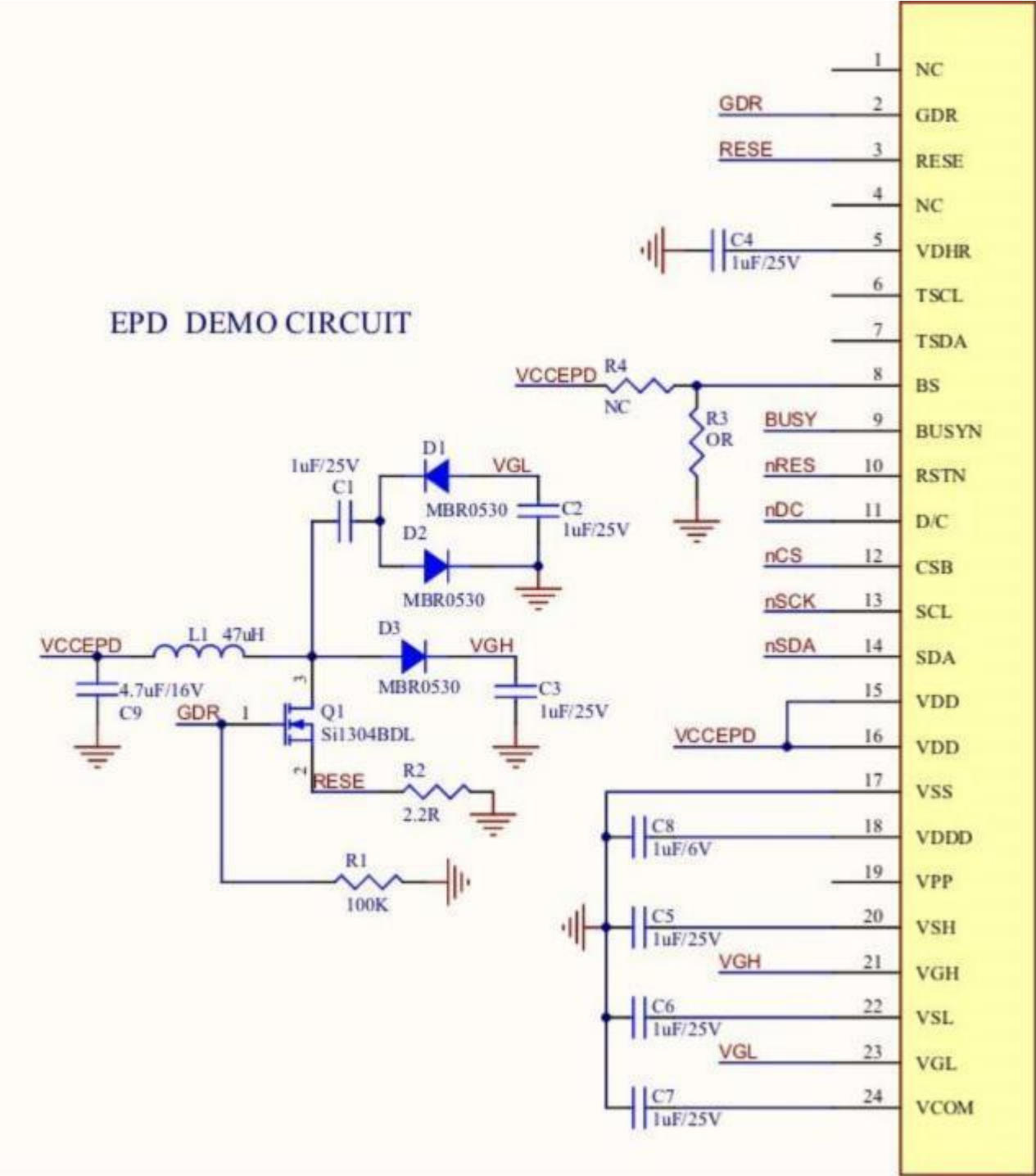
9. Reliability Test

No.	Test	Condition	Method	Remark
1	High Temperature Operation	T = +50°C, RH = 30% for 168 hrs	IEC 60 068-2-2Bp	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
2	Low Temperature Operation	T = 0°C for 168 hrs	IEC 60 068-2-2Ab	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
3	High Temperature Storage	T = +70°C, RH=23% for 168 hrs	IEC 60 068-2-2Bp	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
4	Low Temperature Storage	T = -25°C for 168 hrs	IEC 60 068-2-1Ab	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
5	High Temperature, High Humidity Operation	T = +40°C, RH = 90% for 168 hrs	IEC 60 068-2-3CA	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
6	High Temperature, High Humidity Storage	T = +60°C, RH=80% for 168hrs	IEC 60 068-2-3CA	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
7	Thermal Shock	1 cycle:[-25°C 30min]→[+70°C 30 min] : 50 cycles	IEC 60 068-2-14	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
8	Package Vibration	1.04G, Frequency: 10~500Hz Direction: X,Y,Z Duration: 1 hours in each direction	Full packed for shipment	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
9	Package Drop Impact	Drop from height of 122 cm on concrete surface. Drop sequence: 1 corner, 3edges, 6 faces One drop for each	Full packed for shipment	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.
10	Electrostatic Effect (non-operating)	Machine model +/- 250V, 0Ω, 200pF	IEC 62179 IEC 62180	At the end of the test, electrical, mechanical, and optical specifications shall be satisfied.

10.Block Diagram



11. Typical Application Circuit with SPI Interface



12. Packaging

TBD

13. Mark and Bar Code Definition

TBD

